Control flow-sensitive optimizations Matías

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In the Druid Meta-Compiler

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Conditional branches are slow



Conditional branches are slow... But why?

- Complexification of control flow
- Increase in code size
- CPU pipeline stalling



Complexification¹ of control flow

¹<u>https://english.stackexchange.com/a/607869</u>

- Prevents optimizations
- Makes some compilers' tasks harder
 - Block placement
 - Register allocation

Increase in code size



Has a considerable impact, especially with suboptimal code placement

Pipeline stalling

- Caused by the way modern processors work
- Can have a huge impact

What's a CPU pipeline?

```
r1 := Add(r5, r8)
r2 := Mul(r5, r3)
r3 := Sub(r2, 5)
```

Assuming 3 cycles per instruction

What's a CPU pipeline?



Fetch

Decode

Execute



Decode

Execute



Execute







The issue with branches



And it gets worse...

- The deeper the pipeline is, the longer the stall
 - AMD Zen uses 19 stages
 - Intel Lion Cove uses 10 stages
- Other CPU features can make this even more costly (see indirect branches, superscalar microarchitectures, etc)

CPUs try to solve this

- Branch prediction:
 - Guess which branch will run, and start executing it
 - Discard results if the guess was wrong
- Eager speculative execution
 - Execute **both** branches simultaneously
 - Discard the results from the one that ends up being "wrong"

Still, the best branch is no branch at all

"But... I don't write redundant conditionals!!"

Sadly, compilers write them for you

- Function inlining
- Lowering of high-level features. E.g.,
 - Array bounds checks
 - Runtime type checks
 - Polymorphism + message sends

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- Function inlining
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 - Array bounds checks
 - Runtime type checks
 - Polymorphism + message sends

```
[ i < array size ] whileTrue: [
   var := array at: i.
   i := i + 1.</pre>
```

```
Start:
  jumpIf (i >= array size)
    to End
  jumpIf (i >= array size)
    to Error
 var := MemLoad(array + i)
  i := Add(i, 1)
jumpTo Start
End: ...
```

Goal: Detect and eliminate dead branches

Dead branches: cannot be reached in any execution of the program.

Detecting them implies determining if a given condition is satisfiable in its context

```
x < 5 ifTrue: [
    x > 10 ifTrue: [
        "Unreachable code"
]]
```

PiNodes: Representing Constraints on Variables

PiNodes: Representing Constraints on Variables



Optimizing with PiNodes



Instructions Control Flow Graph (CFG) Conditional branch Loop Graph based representation of a program Nodes = **Basic blocks** Edges = Jumps Basic blocks

SSA

• Variables are assigned exactly once



SSA

- Variables are assigned exactly once
- Φ-functions represent variables at merge points



SSA - use-def chains



PiNodes: representing constraints on variables

```
x<sub>1</sub>< 5 ifTrue: [
x < 5 ifTrue: [</pre>
                                                x_2 := \pi(x_1, <5)
x_2 > 10 ifTrue: [
    x > 10 ifTrue: [
         x doSomething.
                                                       x_3 := \pi(x_2, >10)
x_3 doSomething.
          "Unreachable code"
                                                        "Unreachable code"
```

Optimizations - Dead branch elimination



x3:=
$$\pi$$
(x2, <10) and x2:= π (x1, <5)
Is (- ∞ ; 5) \cap (- ∞ ; 10) empty?
NO \Rightarrow Reachable





```
x1 < 5 ifTrue: [</pre>
    x2 := \pi(x1, <5).
.- x∠.
ifFalse: [
  x3 := π(x1, >=5).
  y2 := 8.
  y1 := x2.
y3 := \Phi(y1, y2)
```

What are the possible values of y3?

The union between the possible values of y1 and y2 $(-\infty; 5) \cup \{8\}$

ABCD method

More powerful:

- Models the relationship between variables
- Models the effect of basic arithmetic operations (addition and subtraction)

ABCD: Eliminating Array Bounds Checks on Demand

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Abstract

To guarantee typesafe execution, Java and other strongly typed lan-

Bounds checks cause programs to execute slower for two reasons. One is the cost of executing the bounds checks themselves since they can occur quite frequently and involve a memory load

ABCD method

x1 <= y1 ifTrue: [</pre> x2 := $\pi(x1, <=y1)$. y2 := π (y1, >=x1). x3 := x2 - 10. x3 < y2 ifTrue: ["tautology"</pre> x4 := π (x3, <y2). y3 := π (y2, >x3).] ifFalse: ["unreachable" x5 := $\pi(x3, >=y2)$. $y4 := \pi(y2, <=x3).$ | |.

Nodes represent SSA values



Experiments and results

Experimental Context



- source-to-source meta-compiler
- uses many optimization passes
 - Analysis and code transformation

Old DBE vs PiNodes

- Druid already had a DBE pass
- Worked by computing all paths a variable was alive in
- Questions:
 - Is our new constant DBE method faster?
 - Is ABCD, the more powerful method, slower?

Measuring Compile Time Improvement

- Used two benchmarks to compare time spent optimizing:
 - Compiled all methods of a test class
 - Compiled one hand-crafted method with an intentionally complex control flow

Results

Relative time spent in the optimization (lower is better)



Results

Relative time spent in the optimization (lower is better)



Results

Relative time spent in the optimization (lower is better)



Future work

Future Work

• Stronger constraint solving - Z3

• Measuring run time improvements

- Looking for more optimization opportunities
 - Using the Druid optimizer for high-level Pharo code
 - Message splitting

More Opportunities for Complex Control Flows

- x < 3 ifTrue: [</pre>
 - y doSomething.

x < 5 ifTrue: [</pre>

|.

|.

z doSomethingElse.



Future: Message splitting

- x < 3 ifTrue: [</pre>
 - y doSomething.

- x < 5 ifTrue: [
 - z doSomethingElse.

Iterative Type Analysis and Extended Message Splitting: Optimizing Dynamically-Typed Object-Oriented Programs*

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X < 3 ? False True X < 5 ? False True

Future: Message splitting



Future: Message splitting



Future: Message splitting



Conclusions

- Branches make code slow
- It's common to have some dead branches in your code
- PiNodes represent constraints on SSA variables, and can be used for DBE



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Addendum

Critical Edges

- Edges whose successor has multiple predecessors, and whose predecessor has multiple successors
- They are annoying for PiNode insertion, because the successor is not dominated by the block containing the condition

Breaking Critical Edges

- Remove that edge, and insert
- Insert a new basic block with just an unconditional jump to the critical edge's target in its place



Domination

 B₁ dominates B₂ if every path from the entry node to B₂ must go through B₁

- B_1 dominates B_1 , B_2 , B_3 , B_4 , B_5
- B_2 dominates B_2 , B_3
- B_{3}, B_{4}, B_{5} only dominate themselves



The PiNode Framework - insertion



- Break critical edges
- Insert PiNodes in each successor of a condition (one for each variable involved)
- Replace usages in dominated blocks

The PiNode Framework - deletion



Simple copy propagation algorithm: replace each usage of the PiNode for a usage of the copied variable

Dead branch elimination

Basic pseudocode of the algorithm

cfg piNodesDo: [:piNode | piNode ifNotSatisfiable: [unreachableBlocks add: piNode basicBlock. 1. cfg removeJmpsTo: unreachableBlocks. cfg removeBlocks: unreachableBlocks.

Message splitting (with code)

```
x < 3 ifTrue: [</pre>
   y doSomething.
] ifFalse: [].
x < 5 ifTrue: [
    z doSomethingElse.
Ι.
```

```
x < 3 ifTrue: [</pre>
    y doSomething.
    x < 5 ifTrue: [
         z doSomethingElse.
     ].
] ifFalse: [
    x < 5 ifTrue: [</pre>
         z doSomethingElse.
     ].
].
```

Message splitting (with code)

x < 3 ifTrue: [</pre> y doSomething.] ifFalse: []. x < 5 ifTrue: [z doSomethingElse. |.

